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PATENT FILING TRANSMITTAL

Transmitted herewith for filing is the Patent Application of: Mitchell S. Cohen, David P. Gaio, Willaim K. Hogan, and Jeannine M. Trehwella

For: OPTICAL FIBER SPACE TRANSFORMATION

TYPE OF FILING

This new patent application is for a(n):

- ☒ Utility
☐ Design
☐ Plant
☐ Divisional
☐ Continuation
☐ Continuation-in-part

Benefit of a prior filed application

- ☐ This application claims the benefit of an earlier filed U.S. Patent Application under 35 USC 120.
☐ Please accord Applicant the benefit of the priority date of _____ to this case pursuant to 35 USC 119. Applicant's claim for priority is based on application _____ filed in _____ on that date.

Filing under 37 CFR 1.53 (Utility) or 37 CFR 1.153 (Design)

- ☒ This is an application filed pursuant to 37 CFR 1.53 or 37 CFR 1.153, permitting receipt of a filing date upon filing of a specification, at least one claim and necessary drawings.
☒ In the event any parts of this application are incomplete, please treat this as a filing under 37 CFR 1.53 or 37 CFR 1.153.

ENCLOSURES

- ☒ 17 - pages of written description;
☒ 7 - pages of claims;
☒ 1 - pages of abstract;
☐ _____ - sheets of formal drawings;
☒ 2 - sheets of informal drawings;
☒ Declaration and Power of Attorney or listing of inventors;
and
☒ Two postcards for return to us as proof of receipt of the above documents.

plus

- ☐ An Assignment of the invention to IBM Corporation and an Assignment cover sheet;
☐ Verified Statement Claiming Small Entity Status (37 CFR 1.9(f) and 1.27(b))

- ☐ Form PTO-1449 (IDS) and two copies of the references listed thereon;
- ☐ A certified copy of ____ (country) patent application number (priority document).
- ☐ A preliminary amendment;
- ☐ Declaration of Biological Deposit;
- ☐ Submission of sequence listing, computer readable copy and/or amendment relating thereto for biotechnology invention containing nucleotide and/or amino acid sequence;
- ☐ An associate power of attorney;
- ☐ Other

DECLARATION OR OATH

The enclosed Declaration or Oath has been executed by:

- ☐ Inventor(s);
- ☐ Legal representative of the inventors (37 CFR 1.42 or 1.43);
- ☐ Joint inventor or person showing proprietary interest on behalf of an inventor who refused to sign or who cannot be reached and this is a petition required by 37 CFR 1.47 and the statement required by 37 CFR 1.47 is attached;
- ☒ Has not been executed and is enclosed for the purposes of identifying the inventors.

INVENTORSHIP STATEMENT

The inventorship for all the claims in this application is:

- ☒ the same;
- ☐ not the same and, as an explanation, a statement is/ will be submitted.

LANGUAGE

The application submitted herewith is:

- ☒ in English;
- ☐ in not in English and in terms of 37 CFR 1.52(d) a verified translation is
 - ☐ attached
 - ☐ not attached.

FEE CALCULATION

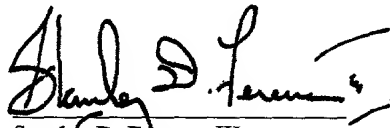
The filing fee has been calculated as shown below:

	SMALL ENTITY	OR	OTHER THAN A SMALL ENTITY	
BASIC FEE Design Patent	\$155	\$	\$310	\$
BASIC FEE Utility Patent	\$345	\$	\$690	\$690
EXTRA FEES	RATE	FEE	RATE	FEE
TOTAL CLAIMS 31 MINUS 20= 11	x 9=	\$0	x18=	\$198
INDEP. CLAIMS 2 MINUS 3 = 0	x 39=	\$0	x78=	\$
<input type="checkbox"/> MULTIPLE DEP. CLAIM	+135=	\$	+270=	\$
<input type="checkbox"/> ASSIGNMENT	+ 40=	\$	+40=	\$
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FEE PAYMENT

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- ☒ Please charge **IBM Corporation Deposit Account No. 50-0510** the amount of \$888.00, to cover the filing fee. Duplicate copies of this letter are enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or credit **Deposit Account No. 50-0510** as required to correct the error.

Respectfully submitted,


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PATENT

Docket No. YOR920000440US1
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Applicant(s) : Mitchell S. COHEN et al. Group Art: not yet assigned
Serial No. : not yet assigned Examiner: not yet assigned
Filed : herewith
For : OPTICAL FIBER SPACE TRANSFORMATION

EXPRESS MAIL CERTIFICATE

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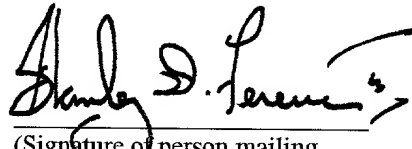
Date of Deposit 26 September 2000

I hereby certify that the following attached paper(s) or fee:

Patent Application
Written Description
Claims 1-31
Abstract
Drawings (Figs. 1-3)
Declaration and Power of Attorney (unexecuted)
Patent Filing Transmittal
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are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service
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person mailing paper)


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OPTICAL FIBER SPACE TRANSFORMATION

Field of the Invention

The present invention generally relates to a method for providing an optical path in the optical subassemblies used in optoelectronic transceivers.

5 Background of the Invention

The optoelectronics industry has made a recent decision to reduce the size of the optical connectors previously used, e.g. the SC connector, to roughly half the size, with corresponding reductions in the size of the transceiver module which mate to the connector. Such a transceiver would mate to an optical connector carrying two fibers,
10 one which carried the outgoing signals from the transmitter, and the other which carried the incoming signals to the receiver. The connectors and transceivers made in accordance with this decision are called "Small Form Factor" (SFF) components. The space allotted to the optoelectronic and supporting chips, circuits and electrical connections, along with the mating optoelectronic interconnection, is typically only about 13 mm X 49.5 mm in the
15 plane, and 9.8 mm high. This requirement puts stringent constraints on the design of the transceiver, particularly because the components packaged within this small space must meet the operating specifications while low fabrication costs are maintained.

Two major complicating factors in the development of transceivers are the facts that:

- no one optical connector has been chosen as the SFF standard. Major contenders for such connectors include the LC, the MTRJ, the VF45, and the SCDC. For a discussion of the various connectors used with SFF (see J. M. Trewhella, "Performance Comparison of Small Form Factor Fiber Optic Connectors", Proceedings of 49th ECTC, pp. 398-407, 1999) and
- most of the optoelectronic chips (laser and receiver) used in the industry are hermetically sealed in TO cans. The use of these cans presents extra packaging challenges because they are large and awkward on the SFF scale, and are typically made with very wide tolerances.

The fabrication of a transceiver which mates to the LC connector has been described previously (W. Hogan, "A Novel Low-Cost Small-Form-Factor Transceiver Module", Proc. 50th ECTC, pp. 725-732, 2000). However, the packaging problem associated with the LC connector is simpler with than that of any other connector, primarily because of the LC's relatively wide fiber-to-fiber spacing (transmitter fiber to receiver fiber) in the connector, i.e. 6.25 mm, which permits the TO cans to be positioned side by side. No such solution is viable for the MTRJ connector, because that connector

has a fiber-fiber spacing of only 750 micrometer, while the width of a TO46 can is about 5.4 mm. Clearly, an optical space transform must be made for the MTRJ connector in order to change the 750 micrometer lateral spacing into a spacing compatible with the use of TO cans.

5 In the past, several optoelectronic devices have been built with waveguides constructed by embedding standard optical fibers in the devices, as well as with non-fiber waveguides specially constructed on the device itself. Either technique for waveguide construction may be used for fabricating a transceiver which mates to an MTRJ connector. In particular, a silicon substrate positioned in the transceiver may be used as the carrier of
10 an “optical coupler” (OC) which serves to interconnect the fibers in a cable terminated by an MTRJ connector to the optoelectronic chips (laser and photodetector). This optical coupler supports the waveguides which are used for the optical interconnect.

 In particular, V-shaped grooves are precision-etched in silicon to carry two optical fibers using known silicon-optical bench (SiOB) techniques, one each for the transmitter
15 and receiver functions, while larger grooves are simultaneously etched to carry the 750 micrometer diameter pins needed for interfacing to the MTRJ connector. The fibers are anchored to the Si carrier by means of epoxy; the ends of the assembly are then polished to provide the finish required by the fiber faces.

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However, because of severe space limitations, the required space transform must be confined to a very short longitudinal distance, which consequently requires that the fibers be subjected to a very small bend radius. Small fiber bend radii are usually unacceptable, however, since extensive studies have shown fiber failure under conditions of small bend radius because of stress-induced cracking (see V. Annovazzi-Lodi et al, "Statistical Analysis of Fiber Failures under Bending-Stress Fatigue", J. Lightwave Tech., vol. 15, pp. 288-293, 1997).

In view of the foregoing, a need has been recognized, *inter alia*, in connection with overcoming the shortcomings and disadvantages encountered in connection with the conventional arrangements discussed above.

Summary of the Invention

In accordance with at least one presently preferred embodiment of the present invention, novel features based on silicon-optical bench (SiOB) technology are contemplated in order to achieve reliable, low-cost optical couplers suitable for use with an MTRJ connector. Particularly, whereas the anchoring of fibers in a SiOB is known, the present invention broadly contemplates the imposition of bends in each fiber, and in a manner that avoids the risk of fiber failure. Also contemplated herein is the use of waveguides instead of fibers.

At least one embodiment of the present invention provides an apparatus for guiding at least one optical path for an optoelectronic transceiver, the apparatus comprising: an input interface; an output interface; and at least one bent element being disposed between the input and output interfaces; the at least one bent element being
5 adapted to provide at least one optical path; the at least one bent element being adapted to avoid premature mechanical failure.

Further, at least one embodiment of the present invention provides a method of forming apparatus for guiding at least one optical path for an optoelectronic transceiver, the method comprising the steps of: providing an input interface; providing an output
10 interface; disposing at least one bent element between the input and output interfaces; adapting the at least one bent element to provide at least one optical path; and adapting the at least one bent element to avoid premature mechanical failure.

For a better understanding of the present invention, together with other and further features and advantages thereof, reference is made to the following description, taken in
15 conjunction with the accompanying drawings, and the scope of the invention will be pointed out in the appended claims.

Brief Description of the Drawings

Figs. 1a and 1b illustrate side and frontal views, respectively, of an optical coupler;

Fig. 2 illustrates a waveguide formed via a wet HF etch; and

Fig. 3 illustrates a comparison of an improved waveguide profile with both an ideal profile and that contemplated by Fig. 2.

5 **Description of the Preferred Embodiments**

Some of the terminology utilized throughout this disclosure, as well as some pertinent background concepts, are described in detail in commonly assigned and copending U.S. Patent Application Serial No. 09/348,955, filed July 7, 1999, entitled “Small Form Factor Optoelectronic Transceivers”, and which is hereby fully incorporated
10 by reference as if set forth in its entirety herein.

Figs. 1a and 1b illustrate an optical coupler (90) and convey two different concepts according to an embodiment of the present invention. Particularly, as discussed below, the components indicated at 106, 108 may be considered to be either fibers or waveguides.

As shown in Figs. 1a and 1b, V-shaped grooves 92, 94, are preferably precision
15 etched in a silicon base 95 to carry two fibers 106, 108. The etching is preferably undertaken using known SiOB techniques. One fiber each is preferably provided for the transmitter and receiver functions, while larger grooves 96, 98 are simultaneously etched

in the base 95 and interfacing cover 99 to carry the pins 110, 112 (such as typically used for interfacing to the MTRJ connector).

Preferably, the precision V grooves (92, 94, 96, 98) are etched only in narrow plateaus 102, 104 in the base of the OC, so that each fiber is anchored only at each end,
5 and is free to take an S-shaped curve between the anchor points. To achieve this shape, the following procedure can be employed:

1. Using an appropriately configured jig, the fibers 106, 108, with the buffer fully intact, are aligned with their anchoring grooves in the base of the OC.
- 10 2. The (preferably) Si cover 99 is clamped to the base 95, thereby temporarily but firmly holding the fibers 106, 108 in place.
3. The assembly is baked in an oven in air at 1000-1100°C for about 15 min.
4. The protruding portions of the fibers 106, 108 are cut if necessary,
15 then the faces of the OC 90 are polished.
5. The metal MTRJ guide pins 110, 112 are inserted and epoxied in place. If desired, the fibers 106, 108 may also be potted in epoxy.

With the exception of the last step, the above operations may preferably be performed in batch mode in order to reduce the cost.

A major innovation is represented by step 3 above. Baking accomplishes several goals:

5 ☒ The stress in the fiber is relieved, so the short bend radii (e.g., 2.5 mm) are achieved under strain-free conditions.

☒ The buffer layer is burned off, leaving clean, bare fiber.

☒ The fibers are anchored to the base and cover by the well known SiO₂ sintering technique used for wafer-wafer bonding.

10 ☒ The cover and base of the OC are simultaneously sintered together.

It is possible to position the two TO cans containing the laser and receiver chips side by side, adjacent fiber anchor 104, if an optical coupler such as that described above is used. Of course, a lens should be positioned between the end of each fiber and its corresponding TO can in order to transfer the light efficiently between the optoelectronic
15 chip in the can and the fiber end.

Another embodiment of the present invention, also conveyed by the illustration in Fig. 1, is also contemplated, particularly, involving the use of waveguides as components 104, 106 instead of fibers.

Again, a silicon substrate is preferably used with anisotropically etched grooves for the alignment pins for the MTRJ connector. However, the fibers discussed previously are replaced by multimode waveguides (106, 108) having the same S shape shown in Fig. 1, but which are processed in parallel over the entire wafer (e.g., a 200 mm diameter silicon wafer). If waveguides are used, very small bending radii should be attainable (less than the 2.5 mm previously considered for fibers) so that the optical coupler could be shortened, thereby conserving space.

It can be recognized that the difficulty in using a waveguide-based optical coupler lies in finding a reliable, inexpensive fabrication technique which will give low-loss waveguides with reproducible properties, and which will ensure that the waveguides are well aligned to the alignment pins. Another recognizable problem is in finding a path for development of a waveguide-based optical coupler in a short time, which means that technology which is easily accessible must be used.

Various approaches have been used in the past to fabricate multimode waveguides, but these approaches have been found to have drawbacks:

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- 10 -

4. A new class of materials known as inorganic-organic photopolymers (ORMOCERs) has recently been used to make waveguides, both single and multimode. M. E. Robertson et al, "O/e-MCM Packaging with new, patternable dielectric and optical materials", Proc. 48th ECTC, pp. 1413-1421, 1998. Fairly vertical walls are achieved because of the photosensitivity of ORMOCERs, and loss values at 850 nm of 0.2 db/cm have been claimed for multimode waveguides. In addition, an MT type connector has been developed for use with such waveguides. This technology may have great promise, however, it is still in the development stage and at this point its practicality is far from assured.

In contrast, in accordance with at least one presently preferred embodiment of the present invention, a waveguide is preferably constructed of a glass core surrounded by polymer cladding. This waveguide structure is fabricated by very straightforward, standard semiconductor-processing techniques.

A key feature of this method is the fabrication of the glass core not by building up the glass, e.g., by flame hydrolysis, but by incorporating a monolithic piece of thin glass into the OC. Such glass has recently been developed and marketed in response to demands by the LCD technology by Schott. Schott D263T glass can be purchased in

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5 sheets of 50 micrometer thickness, with standard lateral dimensions of 280 mm X 415 mm, has excellent transmission and a refractive index of about 1.53. If thinner waveguides are desired, the glass could be ground and polished after step 3 of the procedure outlined below. While such thin glass can of course be shattered if dropped, its handling is usually surprisingly easy.

Some presently contemplated process steps for fabrication of this version of the optical coupler are:

- 10 1. The V grooves 96, 98 (Fig. 1a) for the alignment pins of the MTRJ connector are first etched into the silicon wafer, assumed to be 200 mm in diameter. This step is preferably identical to that previously described for the embedded-fiber OC, except that fiducial marks positioned near the wafer edge are etched simultaneously with the V grooves; these fiducial marks are used for alignment in subsequent masking operations.
- 15 2. The thin glass piece is spin coated with a transparent, low-index adhesive to a thickness of 2-5 micrometer, then positioned so the adhesive is in intimate contact with the silicon wafer. This procedure is best carried out in vacuum to avoid entrapment of air.

3. The adhesive is cured, thereby firmly bonding the glass to the silicon. The adhesive layer not only performs the bonding function, but also serves as the cladding on the underside (silicon side) of the waveguide when the coupler is completed.
- 5 4. An etch-masking layer is deposited on the glass in such a fashion that the fiducial marks in the silicon are still visible.
5. A photoresist layer is spin coated over the etch-masking layer. A photomask is aligned to the fiducial marks in the silicon wafer, and the photoresist is exposed and developed, then the etch-masking layer is delineated so that a replica of the desired waveguide geometry is produced (see Fig. 1).
- 10 6. The waveguide is etched out of the glass sheet with the aid of the patterned etch-masking layer.
7. All photoresist and etch-masking material is removed.
- 15 8. Low-refractive-index UV epoxy is flowed over the entire surface in order to form the side and upper cladding layer.

9. A photomask is placed over the wafer for exposure in a standard mask aligner. The mask is open only in the areas immediately surrounding the delineated waveguides. . The UV epoxy is exposed, then the uncured epoxy in the regions far from the waveguides is removed with solvent.
- 5 10. The optical couplers are diced from the wafer using a standard dicing saw. (Each wafer should yield more than 500 optical couplers). The individual couplers are stacked together for simultaneous polishing of the waveguide faces.

In connection with the above procedure, small-bend radii and hence short couplers

10 with low bending loss are possible because the difference in refractive index between the core and cladding can be made large. Furthermore, the difficult problem of delineating both the waveguides 106, 108 and the V grooves 96, 98 for the pins is solved in a straightforward manner. If a conventional process is used to delineate the waveguides which requires photoresist to be applied *after* the V grooves are etched, severe

15 nonuniformities in photoresist thickness would be encountered due to streaking effects. On the other hand, if, in a conventional process, the waveguides are delineated *before* etching the V grooves, the waveguides and their undercladding could be damaged by exposure to the caustic action of the silicon etchant.

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The adhesive used in step 2 above could be a thermally cured epoxy, a polyimide, or perhaps the same UV epoxy used in step 8 above. One candidate for the latter might be Luxtrak 4039 from Ablestik, which has a refractive index of 1.455, or alternatively OG134 from Epotek, which has a refractive index of 1.416. The UV epoxy could be cured in a
5 mask aligner in both steps.

An important step in the process described above is the etching of the waveguide, since vertical walls are desired. The preferred procedure would be RIE which is known to give vertical walls. While RIE is notoriously slow for glass, if C_2F_6 is used as the reactive gas in this procedure etch rates of 1-2 micrometer/min can be achieved. In this case,
10 photoresist itself could be a good candidate for an etch mask without the use of a separate etch-stop layer, but then a thick photoresist layer would be required because the etch mask would also be eroded away by the RIE. Such a photoresist layer can be made from Dupont's Riston.

On the other hand, if for some reason the RIE process is found to be too expensive
15 because of excessive RIE times, the RIE could be combined with a previous isotropic wet etch in warm HF to give fairly straight walls (but not as straight as a pure RIE process). In this case the initial HF etch will undercut the wet-etch-stop and form an undesirable

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concave waveguide boundary, as illustrated in Fig. 2. (The photoresist is indicated at 150 while the etch profile is indicated, in dotted lines, at 152.)

However, if the waveguide is also furnished with an RIE etch-masking layer, and the wet etching is followed by an RIE procedure which removes, for instance, only 10 micrometer vertical depth of the excess "tails" of the glass waveguide, the profile is improved as shown in Fig. 3. Particularly, Fig. 3 shows the improved profile at 154, as contrasted with an "ideal" profile (156) and the undesirably concave profile (152) from Fig. 2. Thus, the use of both wet etch and RIE yields a "compromise" waveguide profile (154) at a reasonable cost.

10 It is to be understood that, while coupling to an MTRJ connector is emphasized in the present disclosure, various concepts discussed herein may also be applied to other connector types as well.

If not otherwise stated herein, it is to be assumed that all patents, patent applications, patent publications and other publications (including web-based publications) 15 mentioned and cited herein are hereby fully incorporated by reference herein as if set forth in their entirety herein.

Claims

What is claimed is:

1. An apparatus for guiding at least one optical path for an optoelectronic transceiver, said apparatus comprising:

5 an input interface;

 an output interface; and

 at least one bent element being disposed between said input and output interfaces;

 said at least one bent element being adapted to provide at least one optical path;

 said at least one bent element being adapted to avoid premature mechanical failure.

10 2. The apparatus according to Claim 1, wherein said at least one bent element comprises at least one optoelectronic fiber.

 3. The apparatus according to Claim 2, wherein said at least one optoelectronic fiber is integrally adhered to said input and output interfaces.

 4. The apparatus according to Claim 3, wherein said at least one optoelectronic
15 fiber is integrally adhered to said input and output interfaces via baking.

5. The apparatus according to Claim 1, wherein said at least one bent element comprises at least one waveguide.

6. The apparatus according to Claim 5, wherein said at least one waveguide is etched from at least one glass sheet.

5 7. The apparatus according to Claim 1, wherein said input and output interfaces comprise at least one V-shaped groove which accepts at least one end of at least one said bent element.

8. The apparatus according to Claim 1, wherein said at least one bent element comprises one element for providing optoelectronic transmission and one element for
10 providing optoelectronic reception.

9. The apparatus according to Claim 1, wherein said at least one bent element includes a bending radius of less than about 2.5 mm.

10. The apparatus according to Claim 1, wherein at least one of said input interface and said output interface comprises silicon.

15 11. The apparatus according to Claim 1, said apparatus comprising a base element and a cover element, said base element and said cover element combining to form said input interface and said output interface.

12. A method of forming apparatus for guiding at least one optical path for an optoelectronic transceiver, said method comprising the steps of:

providing an input interface;

providing an output interface;

5 disposing at least one bent element between said input and output interfaces;

adapting said at least one bent element to provide at least one optical path; and

adapting said at least one bent element to avoid premature mechanical failure.

13. The method according to Claim 12, wherein said step of providing at least one bent element comprises providing at least one optoelectronic fiber.

10 14. The method according to Claim 13, wherein said disposing step comprises integrally adhering said at least one optoelectronic fiber to said input and output interfaces.

15 15. The method according to Claim 13, wherein said step of adapting said at least one bent element to avoid premature mechanical failure comprises baking said at least one optoelectronic fiber.

16. The method according to Claim 15, wherein said step of baking comprises baking in an oven in air at between about 1000 and about 1100 degrees Celsius.

17. The method according to Claim 16, wherein the duration of said baking step is about 15 minutes.

5 18. The method according to Claim 15, wherein:

said step of providing at least one optoelectronic fiber comprises providing a buffer layer on said at least one optoelectronic fiber; and

during said baking step, said buffer layer is burned off.

19. The method according to Claim 15, wherein:

10 at least one of said input interface and said output interface comprises silicon;

said baking step comprises anchoring said at least one optoelectronic fiber to at least one of said input interface and said output interface via SiO₂ sintering.

20. The method according to Claim 15, wherein said steps of providing an input interface and said step of providing an output interface comprise providing a base element and a cover element, said base element and said cover element combining to form said input interface and said output interface.

15

21. The method according to Claim 20, wherein:

said cover element and said base element comprise silicon;

said baking step comprises anchoring said cover element and said base element to each other via SiO₂ sintering.

5 22. The method according to Claim 12, wherein said step of providing at least one bent element comprises providing at least one waveguide.

23. The method according to Claim 22, wherein said step of providing at least one waveguide comprises etching at least one waveguide from at least one glass sheet.

24. The method according to Claim 23, wherein said step of providing at least one
10 waveguide further comprises:

providing a silicon wafer; and

mounting said at least one glass sheet on said silicon wafer.

25. The method according to Claim 24, wherein said mounting step comprises
providing an adhesive and bonding said at least one glass sheet to said silicon wafer using
15 said adhesive.

26. The method according to Claim 22, wherein said step of providing at least one waveguide further comprises the step of depositing an etch-masking layer on said at least one glass sheet.

27. The method according to Claim 26, wherein said step of providing at least one waveguide further comprises the steps of:

providing a photoresist layer is over said etch-masking layer;

exposing and developing said photoresist layer; and

delineating said etch-masking layer to produce a replica of desired geometry for said at least one waveguide.

28. The method according to Claim 27, wherein said etching step further comprises the step of etching said at least one waveguide out of said at least one glass sheet with the aid of the patterned etch-masking layer.

29. The method according to Claim 12, wherein said disposing step comprises:

providing at least one V-shaped groove in at least one of said input and output interfaces; and

disposing at least one end of at least one said bent element in said at least one V-shaped groove.

30. The method according to Claim 12, wherein said step of providing at least one bent element comprises providing one element for providing optoelectronic transmission
5 and one element for providing optoelectronic reception.

31. The method according to Claim 12, wherein said step of providing at least one bent element comprises providing at least one bent element that includes a bending radius of less than about 2.5 mm.

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Methods and arrangements for providing compact optical paths between an optical connector, such as an MTRJ connector, and optoelectronic chips contained in TO cans.

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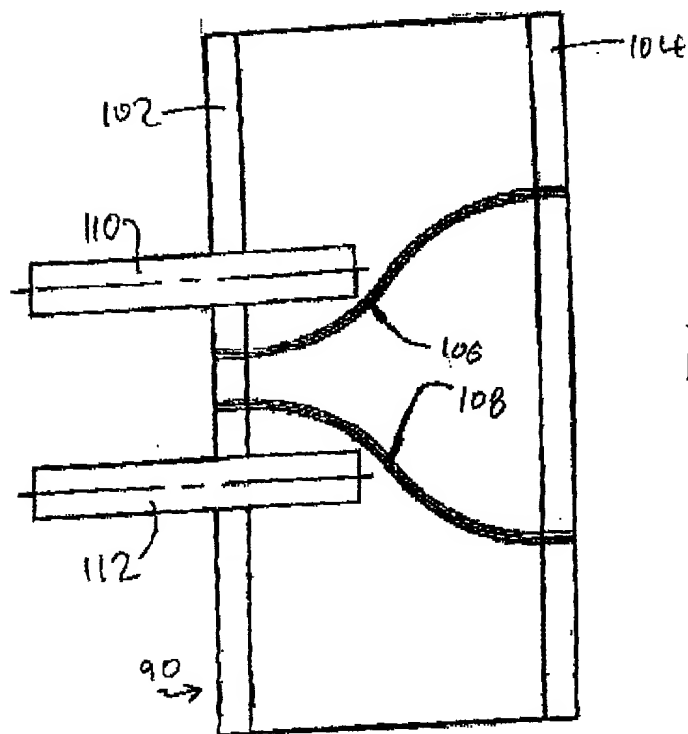


FIG. 1a

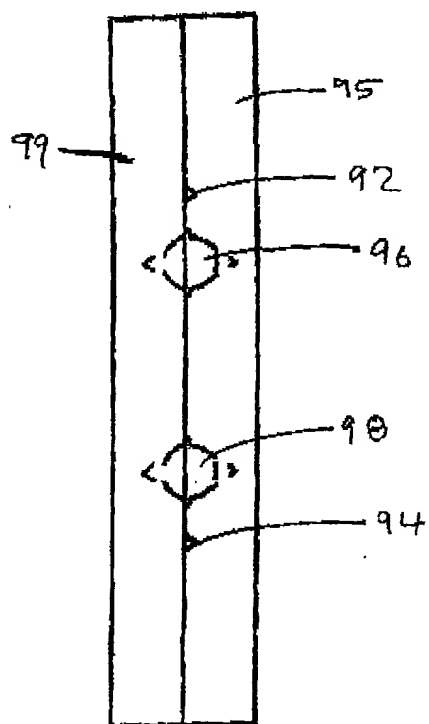


FIG. 1B.

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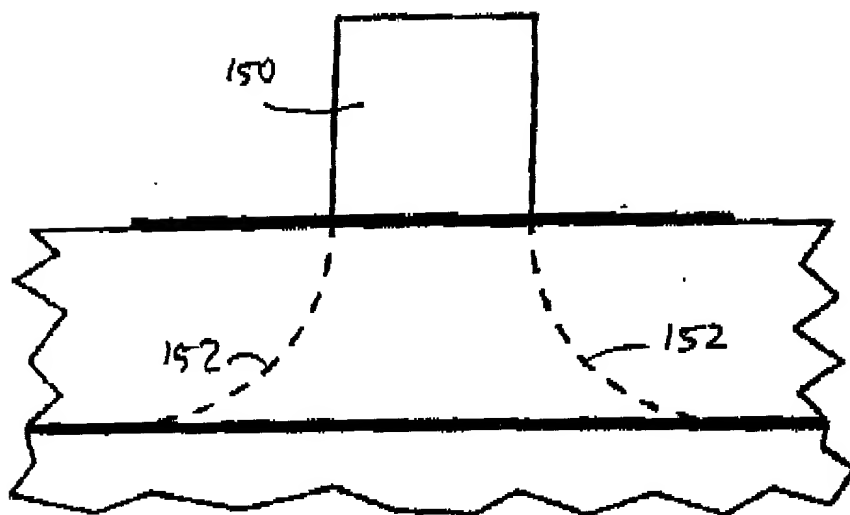


FIG. 2

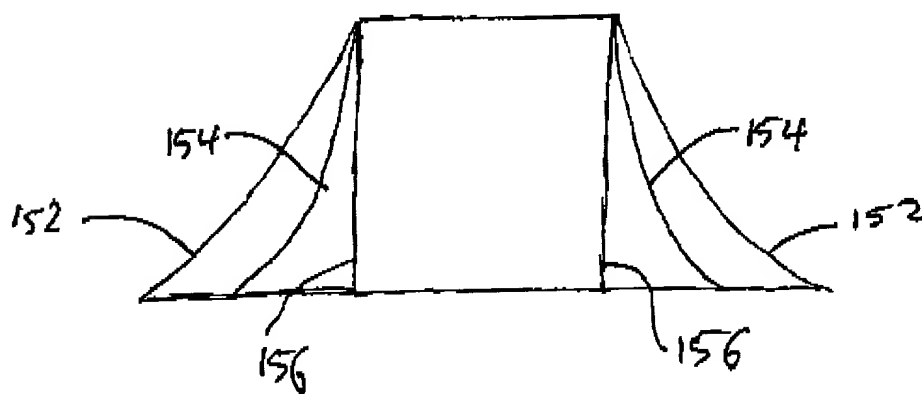


FIG. 3

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DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

OPTICAL FIBER SPACE TRANSFORMATION

the specification of which (check one)

☐ is attached hereto.

☒ was filed on 26 September 2000 as International Business Machines Docket No. YOR920000440US1

and was amended on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) or §365(b) of any foreign application(s) for patent or inventor's certificate, or §365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application, having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No
(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No
(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim the benefit under 35 U.S.C. §119(e) of any United States provisional application(s) listed below.

(Application Number)	(Filing Date)
(Application Number)	(Filing Date)

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

I hereby claim the benefit under 35 U.S.C. §120 of any United States Application(s), or §365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States, or PCT International application in the manner provided by the first paragraph of 35 U.S.C. §112, I acknowledge the duty to disclose information material to the patentability of this application as defined in 37 CFR §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number).

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